34	nBattery_Present_B	In	low level indicates battery existence	

3.2.2.8.13. Interfaces of Main Board and Adapter

The interfaces of the main board and adapter are defined in the following table:

Table 3-22 interfaces of the main board and adapter

No.	Name	Direction
1	Gnd	\
6	Gnd	\
2	Gnd	\
4	Gnd	\
3	+12V	In
7	+12V	In
5	+12V	In
8	+12V	In

3.2.2.8.14. Interfaces of Main Board and Power Board

The interfaces of the main board and power board are defined in Table 3-24 interface signals of main board and power board interfaces of the main board and power board. The signal description is shown in Table 3-24 interface signals of main board and power board. The signal direction is relative to the power board.

Table 3-23 interfaces of the main board and power board

Pin No.	Name	Pin No.	Name
1	EDC_Power	2	EDC_Power
3	EDC_Power	4	EDC_Power
5	GND	6	GND
7	GND	8	GND
9	A_Battery+	10	A_Battery+
11	A_Battery-	12	A_Battery-
13	B_Battery+	14	B_Battery+
15	B_Battery-	16	B_Battery-
17	Gnd	18	Gnd
19	A_Battery_NTC	20	B_Battery_NTC
21	EDC_Status	22	Power_ON
23	Gnd	24	Scan_Status
25	Temperature_D+	26	CW_Mode
27	Temperature_D-	28	+5VStb_CPU_En
29	Gnd	30	Gnd
31	+5VStb	32	+5VStb_CPU
33	+12V	34	+12V
35	Gnd	36	Gnd
37	GND	38	GND
39	-12V	40	-5V
41	Gnd	42	Gnd
43	+5V	44	+5V